



Canada May 5-8, 2025

Conference Website: <http://www.hostsymposium.org/call-for-paper.php>

About the Conference

HOST Symposium

IEEE International Symposium on Hardware Oriented Security and Trust (HOST)

May 5-8, 2025

San Jose, USA

Call for Papers

The IEEE International Symposium on Hardware Oriented Security and Trust (HOST) - the premier event aiming to facilitate the rapid growth of hardware security research and development - has opened its call for contributions. The 18th annual HOST will be held May 5-8, 2025 in San Jose, California, the capital of Silicon Valley. HOST 2025 invites original contributions in all areas of overlap between hardware and security, including but not limited to the following:

Computer-aided Design (CAD) for Hardware Security Verification

e.g., automatic techniques and metrics for life cycle security management and detecting security vulnerabilities.

Hardware Security Primitives

e.g., PUFs, TRNGs, cryptography, post-quantum cryptography, odometers.

Hardware Attack and Defense

e.g., hardware Trojans, fault injection, side-channels, hardware reverse engineering, hardware obfuscation.

Architecture Security

e.g., architectural side-channels, trusted execution environment, FPGA and reconfigurable fabric security.

System security

e.g., machine learning security, SoC/IP security, CPS/IoT security, sensor network security, smart grid security, cloud security.

Emerging Security and Privacy Threats and Solutions

e.g., privacy-enhancing architecture, blockchain, cryptocurrency security, and other hardware/system security topics.

Paper Submission

HOST 2025 has two submission windows. Please submit your paper at <https://easychair.org/conferences/?conf=host2025>. The page limit for submitted papers is 10 pages (excluding references), in double column, IEEE format, with a minimum font size of 10 points. Paper templates are available at <https://www.ieee.org/conferences/publishing/templates.html>. Only work that has not been previously published at the time of the submission will be considered. Duplicate submissions to concurrent conferences/journals are not permissible, and if encountered will be rejected and reported to IEEE. The paper selection will involve a double-blind review - the identity of authors must not be revealed, directly or indirectly, over the course of the entire process, with all references to the author(s)' own previous work being in the third person. Please avoid the use of "omitted for blind review" in the bibliography and make sure that the PDF metadata does not contain author information.

Conflict of Interest Declaration

Before the full paper submission deadline, please declare your conflicts of interest with the TPC members on EasyChair, using the "Declare conflict" link on the top right corner of your paper information page. Conflicts of interest for HOST 2025 include authors of the submission, family members, advisors/students, colleagues from the same institution, and co-authors/collaborators in the past 48 months.

Fall Submission

August 19, 2024 September 9, 2024: Abstract Submission*

August 26, 2024 September 9, 2024: Full Paper Submission

October 27, 2024 November 16, 2024: Notification of Acceptance

November 18, 2024: Camera-ready Version

Winter Submission

December 9, 2024: Abstract Submission*

December 16, 2024: Full Paper Submission

February 17, 2025: Notification of Acceptance

March 17, 2025: Camera-ready Version

*Abstract registration is required - Only the papers registered by the abstract submission deadline can update the submission file by the paper submission deadline.

HOST 2025 also accepts proposals for tutorials and demos. More information will be disseminated in future calls.

Contact Information

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Important Dates

May 05

Conference Date

May 5-8, 2025

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