

🌐 Italy 📅 September 10-12, 2025

CONFERENCE WEBSITE: <https://dsd-seaa.com/dsd2025/>

| About the Conference

28th Euromicro Conference Series on Digital System Design (DSD) 2025 will be held at Grant Hotel, Salerno, Italy, from 10th to 12th of September 2025.

co-located conferences:

51st Euromicro Conference Series on Software Engineering and Advanced Applications (SEAA) 2025 will be held at Grant Hotel, Salerno, Italy, from 10th to 12th of September 2025.

Dates from 2024 advanced to 2025

Important dates

Deadline for abstract submission: 27th May 2025
(AoE) /firm/

Deadline for full paper submission: 05th May 2025
3rd June 2025 (AoE) /firm/

Notification of acceptance: 12th June 2025 28th
June 2025 01th July 2025

Camera ready papers: 26th June 2025 11th July
2025 (AoE)

Important Dates

SEP
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CONFERENCE
DATE
**September
10-12,
2025**

Call for Papers

The Euromicro Conference on Digital System Design (DSD) addresses all aspects of (embedded, pervasive and high-performance) digital and mixed HW/SW system development and covers the entire design process from specification to microarchitectures, digital circuits and VLSI implementations. It is a forum for researchers and engineers from academia and industry working on advanced research, development and applications. It focuses on the current and future challenges of: advanced embedded, high-performance and cyber-physical applications; system and processor architectures for embedded and high performance HW/SW systems; design methodology and design automation for all design levels of embedded, high performance and cyber-physical systems; modern implementation technologies ranging from full-custom circuits in nanometer technology nodes to FPGAs and MPSoC infrastructures. Authors are cordially invited to submit their work on (but not limited to) the topics of the main track of the conference. In addition, contributions to various special sessions (with dedicated coordinators and sub-program committees) on specific topics of special interest are welcome. All papers will be reviewed according to common guidelines, quality requirements and acceptance thresholds.

Main Topics

Artificial intelligence from edge to cloud:
architectures, methods, tools and applications
Design and synthesis of systems, hardware and embedded software – specification, modelling,

analysis, validation and testing

Design automation at system, processor, register-transfer, logic and physical levels

Formal methods in system and hardware design

IoT, cyber-physical, embedded systems and applications

Systems-on-a-chip, networks-on-a-chip and systems-in-a-package

High-performance, energy-efficient multi-core and many-core (heterogeneous) processor architectures.

Autonomous/adaptable/reconfigurable systems and architectures

Security, safety, reliability and multi-objective optimization of embedded and cyber-physical systems

Future trends, new applications and new technologies

Program

Will be announced later.

Registration

Will be announced later.

Venue and travel

Grand Hotel, Salerno, Italy.

More details will be announced later.