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HPCC 2026 : 2026 IEEE International Conference on High Performance Computing and Communications

LOCATION	SUBJECT / TOPIC	EVENT DATES	LAST UPDATED
China	General	October 29 - November 2, 2026	2nd July, 2026

DESCRIPTION

Join us at the 2026 IEEE International Conference on High Performance Computing and Communications

With the rapid growth in computing and communications technology, the past decade has witnessed a proliferation of powerful parallel and distributed systems and an ever increasing demand for practice of high performance computing and communications (HPCC). HPCC has moved into the mainstream of computing and has become a key technology in determining future research and development activities in many academic and industrial branches, especially when the solution of large and complex problems must cope with very tight timing schedules.

Among a series of highly successful International Conferences on High Performance Computing and Communications (HPCC), the HPCC-2026 conference is a leading forum for engineers and scientists in academia, industry, and government to address the resulting profound challenges and to present and discuss their new ideas, research results, applications and experience on all aspects of high performance computing and communications. IEEE HPCC-2026 is sponsored by IEEE, IEEE Computer Society, and IEEE Technical Committee on Scalable Computing (TCSC).

IEEE HPCC-2026 follows the tradition of previous successful IEEE HPCC conferences held from 2005 to 2025 in Asia, Europe, Australia, and America. It will feature sessions of regular presentations, workshops, tutorials and keynote speeches. IEEE HPCC-2026 will be hosted in Luoyang, China, a historic city known for its rich cultural heritage and as one of the ancient capitals of China. Luoyang is home to numerous historical landmarks, including the renowned Longmen Grottoes, a UNESCO World Heritage Site, and the famous White Horse Temple, regarded as the cradle of Buddhism in China. The city also offers a harmonious blend of tradition and modern development, making

KEY DEADLINES

Submission	**15th Jul, 2026**
Notification	**10th Sep, 2026**
Camera Ready	**30th Sep, 2026**
Conference	**29th Oct, 2026**

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it an attractive destination for international visitors.

Prospective authors are invited to submit their papers to HPCC-2026. Accepted papers will be submitted for inclusion into IEEE Xplore subject to meeting IEEE Xplore's scope and quality requirements. The authors of selected best papers will be invited post conference to extend their contributions for special issues of prestigious journals to be planned in conjunction with the conference.

Topics of interest include, but are not limited to:

Track 1: High Performance Computing and Applications

High Performance Computing Theory
High Performance Computing Architectures
System Software and Middleware
System Software Support for Scientific Workflows
Storage and I/O Systems
Resource Management
Instruction-Level and Thread-Level Parallelism
Performance Modeling and Evaluation
Massively Multicore Systems
Future Novel Computing Platforms
Database Applications and Data Mining
High Performance Computing for Bioinformatics
High Performance Computing for Big Data
High Performance Computing for AI
High Performance Computing for Block Chains
Green High Performance Computing
Neural Network Optimization Techniques

Track 2: Parallel and Distributed Computing and Systems

Parallel and Distributed System Architectures
Parallel and Distributed Algorithms
Data Center Architectures
Resource Virtualization
Web Services and Internet Computing
Computational Models and Algorithms
Image Recognition and Processing
Cloud Computing
Federated Learning
Embedded Systems
Distributed Systems and Applications
Pervasive/Ubiquitous Computing & Intelligence
Distributed Graphics and VR/AR/MR Systems
Distributed AI and Soft/Natural Computing
Power-Efficient and Green Computing Systems
Parallel and Distributed Computing for Big Data
Parallel and Distributed Computing for AI

Track 3: Communications and Networking

Network and Interconnect Architectures
Computer Networks
Internet Architectures and Protocols
Telecommunications
Trust, Security, and Privacy
Energy-Aware Computing and Networking
Machine Learning and Deep Learning
Software Defined Networking
Network Functions Virtualization
Performance Evaluation and Measurement

Track 4: Computing Power Network (CPN) and Network for AI Computing

Network-Compute Convergence Technologies for AI Applications
Resource Allocation and Collaboration in CPN
Computing Power Identification and Measurement Technologies
Lossless Network Design
Technology for RDMA
IPv6/SRv6-Based Hyperscale AI Data Centers
Dynamic Resource Sharing in Network-Compute Systems
Deterministic Networking for AI Computing
Edge-Cloud Collaborative Computing Frameworks
Network-Computing Converged Architecture

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